



## **6 Relay output module TXM1.6R**

URL:<https://www.sxplc.com/6-relay-output-module-txm1-6r>

**Product data sheet**

## Technical data

Supply (bus connector on side)	Operating voltage	DC 21.5 ... 26 V			
	Extra low voltage SELV or PELV in accordance with HD384				
	Max. power consumption	<table border="0"> <tr> <td>TXM1.6R</td> <td>1.7 W</td> </tr> <tr> <td>TXM1.6R-M</td> <td>1.9 W</td> </tr> </table>	TXM1.6R	1.7 W	TXM1.6R-M
TXM1.6R	1.7 W				
TXM1.6R-M	1.9 W				
<i>(for the sizing of power supplies, see CM110562)</i>					
Protection	Bus connector on side	No protection against shortcut and incorrect wiring with AC / DC 24 V			
Switching outputs	Number of switching outputs	6 (changeover contact)			
	External fuse protection for incoming cable				
	<ul style="list-style-type: none"> <li>• Slow blow fusible link</li> <li>• Circuit breaker</li> </ul>	<table border="0"> <tr> <td>Max. 10 A</td> </tr> <tr> <td>Max. 13 A</td> </tr> </table>	Max. 10 A	Max. 13 A	
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Max. 13 A					
Circuit breaker tripping characteristic		Type B, C or D to EN 60898			
Contact data for AC	Voltage range	min. AC 12 V max. AC 250 V			
	Current, resistive load	max. 4 A			
	Current, inductive load (cos phi ≥ 0.6)	max. 3 A			
	Switching current	min. 1 mA at AC 250 V min. 10 mA at AC 12 V			
	Current on make	max. 20 A during max. 10 ms max. 10 A during max. 1 s			
Contact data for DC	For UL applications	3 FLA, 9 LRA, 1/4 HP, 4 (3) A			
	Voltage range	min. DC 12 V, max. DC 30 V			
	Current, resistive load	max. 3 A at DC 30 V min. 10mA at DC 12 V			
	Current on make	max. 3 A			
Service life of contact for AC 250 V	With 0.1 A resistive	8 million switching operations			
	With 0.5 A resistive	2 million switching operations			
	With 4.0 A resistive (N/O)	0.2 million switching operations			
	Reduction factor with inductive load (cos phi ≥ 0.6)	0.6 (max. 3 A inductive)			
Insulation resistance	Reinforced insulation between relay outputs and system electronics	AC 3750 V, to EN 60 730-1			
	Mixed voltages (AC 250 V mains voltage and SELV/PELV 24 V) as well as mixed phases are permitted on adjacent I/O points of the module				

