



operating voltage  $U_B = 20 \dots 30 \text{ V}$ , d.c., reverse polarity protection  
Voltage divider for  $U_{REF}$  in  $U_{REF}$  pin:  $7 \text{ k}\Omega$  +  $0 \text{ }\Omega$  +  $4 \text{ k}\Omega$   
Transport cable with M8 indicator plug in PUR cable, at the transmitter:

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## outputs

output 1: dc output,  $U_{REF}$  circuit-protected  
delay prior to availability  $\leq 500 \text{ ns}$  Mode,  $5,5 \text{ ms}$  im Free-Run-Mode

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## inputs

description  $< U_B + 18 \text{ V}$ : logic 1;  $> -U_B + 13 \text{ V}$  or control input open:  
input 3 control input

